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### **VLSI Digital Signal Processing Chapter 6 Folding**

Dr. Parhi is widely recognized for his work on high-level transformations of iterative data-flow computations, for developing a formal theory of computing for design of digital signal processing systems, and for his contributions to multi-gigabit Ethernet transceivers on copper and fiber and for backplanes.

### **Keshab K. Parhi - Electrical and Computer Engineering**

Keshab K. Parhi is the author of VLSI Digital Signal Processing Systems (4.09 avg rating, 22 ratings, 1 review, published 1999), Pipelined Lattice and Wa...

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